PRELIMINARY AMENDMENT

Serial Number: 10/717862 Filing Date: November 18, 2003

Title: PARTIAL ARRAY SELF-REFRESH

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IN THE ABSTRACT

Please amend the abstract as follows:

Abstract of the Disclosure

A memory device includes an address selection circuit to store addresses of selected rows of memory cells. During a refresh mode, only the memory cells of the selected rows are refreshed. The addresses of the selected rows can be stored by a user or automatically by the memory device during a memory operation mode or manually by a user during a programming mode.